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Docket No.: Bays 10-8-2 [D8143-00206]



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: El-kik et al.

Examiner: David J. Huisman

Serial No.: 09/478,714

Group Art Unit: 2183

Filed: January 6, 2000

For: PROCESSOR SYSTEM INCLUDING INTERNAL ADDRESS GENERATOR FOR IMPLEMENTING SINGLE AND BURST DATA TRANSFERS

I, PATRICIA A. MCINTOSH HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 C.F.R. 1.10 ON THE DATE INDICATED BELOW AND IS ADDRESSED TO THE ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231

on 2/13/03

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BOX AF Assistant Commissioner for Patents Washington, DC 20231

Sir:

## AFTER-FINAL RESPONSE UNDER 37 C.F.R. § 1.116

The following remarks are responsive to the Official Action dated <u>December 17, 2002</u>.

## REMARKS

## **Drawings**

The Examiner objects to Figure 1 under 37 C.F.R. 1.83(a) as failing to show a "RDN" signal line referenced at page 5, lines 18-19 of the specification. Applicant submitted with its November 25, 2002 Amendment a revised version of Figure 1 which showed the RDN signal